

1 (10) ABSTRACT

2 A BiCMOS auxiliary output driver is provided to maintain output logic signal levels when
3 integrated circuit chip power supply voltage is outside its nominal range. When the power
4 supply voltage level is within design tolerance for a MOSFET output driver stage, the
5 auxiliary output driver is off; when below design tolerance, the auxiliary output driver is
6 turned on. Driver stage output pad signal level is maintained at a desired state level by the
7 auxiliary output driver whenever the power supply slips below its design tolerance range.